[illegible]

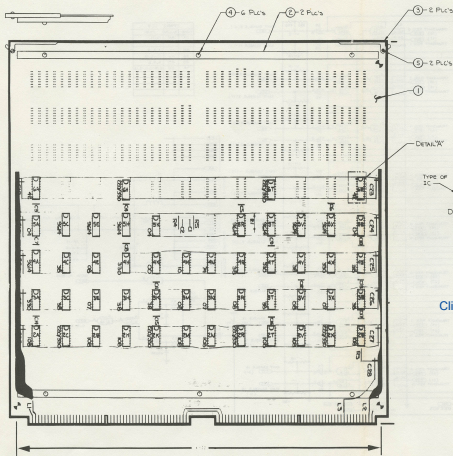
		PLANNED PARTY NO.
PERF-18	DELETE RESOURCES R11=3,15=25,28=50	PA. W. 45-00720 00
PERT-19	DELETE RESOURCES R18,17 DELETE INTEGRATED CIRCUIT A1	PA. W. 45-00721-00
PERT-20	DELETE RESOURCES R11=50 DELETE INTEGRATED CIRCUIT A1	PA. W. 45-00722 00

NOTES

1. LADDER, OTHERWISE SPECIFIED, DELIVER VALUES
RPT. BY COMPS + 57, 114 WATT.

Per Terminal Electronics Part 1A, 1B + 1C Drawing No. D0 1612

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NOTES:

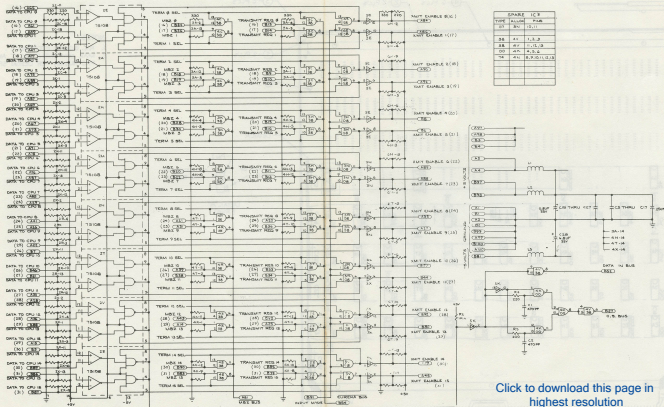
1. P.C. BOARD ETCH REV. 11.
2. MAX COMPONENT HEIGHT AND LEAD PROTRUSION PER DESIGN SPEC-201.

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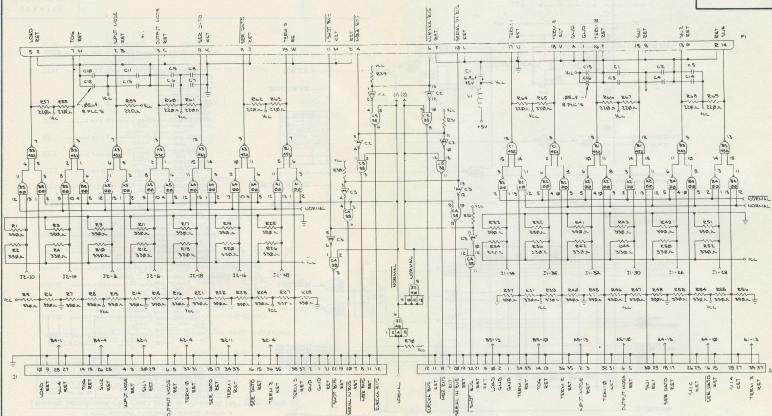
P.C. Assembly Hex Per Term

Drawing No. 01840

Blatt 1 von 1



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1	74LS00	173	9.1
2	74LS10	273	2.0
3	74LS10	273	2.0

Logic Diagram Duplex Board Drawing No. 00134 - Blatt 1 von 1